

REMARKS

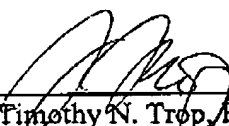
With respect to the prior art rejection, it is noted that some 100 lines of text are cited on page 5, subparagraph b, for the claim language "indicating whether data in said register is available for a given central processing unit by providing different indicators assigned to each of a plurality of central processing units." Nothing in the communication register frame provides different indicators assigned to each of a plurality of central processing units. Nothing indicates whether data in the register is available for a given central processing unit. Therefore, reconsideration is requested.

Further, the claim calls for causing a given central processing unit to reset its indicator when the data in said register is no longer useful to the given central processing unit. In support of the rejection, column 8, lines 29-36, are cited. The forklock flag indicates that either a fork is being posted or taken by the processor and no other processor should access the fork block until it is cleared. This has, obviously, nothing to do with whether the data in the register is no longer useful to a given processor. Therefore, reconsideration of the rejection of claim 1 is respectfully requested.

Similarly, reconsideration of claims 11-17 is requested.

Respectfully submitted,

Date: May 4, 2006



Timothy N. Trop, Reg. No. 28,994
TROP, PRUNER & HU, P.C.
8554 Katy Freeway, Ste. 100
Houston, TX 77024
713/468-8880 [Phone]
713/468-8883 [Fax]

Attorneys for Intel Corporation